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# SYMBOL

318, 219	PG	P16	208, 307
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018	08	P7	210, 317
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014	01		
310, 211	NG		

FC188  
OUTPUT  
GROUP  
SELECT

RECORD OF CHANGES				
DATE	ISS	PERSON	STD	NOTE

## CIRCUIT DESCRIPTION

### FUNCTIONS

THIS CIRCUIT PROVIDES FOR CONTROLLING ONE OUT OF SIXTEEN HIGH CURRENT PULSE PATTERNS FROM RELATIVELY LOW POWER SIGNALS ON INPUTS 61-66.

### DETAILED DESCRIPTION

THE HIGH AMPLITUDE CURRENT PULSE PULSES INTO THE P16 LEAD AND OUT ONE OF THE LEADS P1-P16. THE PARTICULAR LEAD IS SELECTED BY APPLYING 19 VOLTS ON THE CORRESPONDING INPUT LEAD 61-66 AND GROUNDING INPUT LEAD NG.

## NOTES:

1. UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS CAPACITANCE VALUES ARE IN MICROFARADS VALUES PRECEDED BY THE SYMBOL "R" (PLUS) OR "C" (MINUS) ARE IN VOLTS.

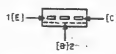
2. POWER AND GROUND TERMINALS FOR INTEGRATED CIRCUITS:

IC CODE	GRID TERM.

3. BATTERY AND GROUND TERMINALS FOR THIS CIRCUIT PACK ARE AS FOLLOWS:

FUNCTION	TERMINAL

4. THE TERMINAL NUMBER ARRANGEMENT OF THE 89A TRANSISTOR IS:



5. CLOSEST HORIZONTAL MOUNTING CENTERS IS 0.770 INCH.

6. INITIAL USE OF THE FC188 CIRCUIT PACK IS IN SD-RH10-01.

SYSTEM USED ON	DESIGN CONTROL
NO. 3 ESS	DI

CURRENT DRAIN: 0 mA

# SYMBOL

OUTPUT GROUP SELECT  
ELEMENT IDENT.

TERM. NO.	FUNC.	TERM.	LOC.	TERM. NO.	FUNC.	TERM.	LOC.
G1	I	014	2F2	P13	g	207	289
G2	I	013	2F2	P13	g	206	2C9
G3	I	012	2E2	P14	g	202	289
G4	I	011	2E2	P14	g	201	289
G5	I	010	2E2	P15	g	231	289
G6	I	016	2C2	P15	g	200	289
G7	I	017	2D2	P16	g	208	289
G8	I	018	2D2	P16	g	207	289
G9	I	104	2D2				
G10	I	102	2C2				
G11	I	101	2C2				
G12	I	100	2C2				
G13	I	105	2D2				
G14	I	106	2D2				
G15	I	107	2D2				
G16	I	108	2D2				
NG	I	211	2F2				
PG	I	219	2D2				
PG	I	318	242				
P1	g	212	2F9				
P1	g	311	2F9				
P2	g	213	2F9				
P2	g	312	2F9				
P3	g	214	2E9				
P4	g	313	2E9				
P5	g	215	2E9				
P6	g	314	2E9				
P5	g	216	2E9				
P6	g	315	2E9				
P6	g	217	2E9				
P6	g	316	2E9				
P7	g	218	2D9				
P7	g	317	2D9				
P8	g	209	2D9				
PC	g	308	2D9				
P9	g	203	2D9				
P9	g	302	2D9				
P10	g	204	2C9				
P10	g	303	2C9				
P11	g	205	2C9				
P11	g	304	2C9				
P12	g	206	2C9				
P12	g	305	2C9				

## SHEET INDEX NOTES

1. FOR SINGLE REISS. S. A CHANGED OR NEW SHEET WILL BE ASSIGNED THE SAME ISSUE NUMBER AS SHEET 1.  
2. FOR CONCURRENT REISSUES, A CHANGED OR NEW SHEET WILL BE ASSIGNED THE HIGHEST ISSUE NUMBER AFFECTING THAT SHEET.  
3. THE ISSUE NUMBER OF SHEET 1 IS RECOGNIZED AS THE ISSUE NUMBER OF THE WHOLE DRAWING.

## SUPPORTING INFORMATION

CATEGORY	NUMBER
CONNECTOR ON FRAME	9479, 947C OR 947E
CIRCUIT PACK INFORMATION DRAWING	
SERIES FOR LATEST CLASS "A" CHANGE	
ACCEPTABLE SERIES	4

NOTICE - NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.

FC188 CIRCUIT PACK  
OUTPUT GROUP SELECT  
CIRCUIT

ATACOG STANDARD
DWG SIZE 63
ISSUE 2D1

BELL LABORATORIES

CPS-FC188

2 SHEETS

# OUTPUT GROUP SELECT CIRCUIT

## COMPONENT LIST

### CAPACITOR

#### DESIG. CODE

[16] C1-C16 KS-20736 L1, .1

### DIODE

#### DESIG. CODE

[16] CR1-CR16 4960  
[17] CR17-CR33 555B

### RESISTOR

#### DESIG. CODE

[16] R1-R16 KS-20810 L16, 542

### TRANSISTOR

#### DESIG. CODE

[16] Q1-Q16 85A

